



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent application of

Taduya Usami

Serial No.: 09/851,313

Group Art Unit: 2823

Filing Date: May 9, 2001

Examiner: Julio J. Maldonado

For: SEMICONDUCTOR DEVICE AND SEMICONDUCTOR WAFER

Honorable Commissioner of Patents
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents listed on the attached from PTO-1449, to the attention of the Examiner in charge of the above-identified application. These references were cited by the Examiner in the divisional Application No. 10/135,393, in an Office Action dated October 6, 2003.

This citation does not constitute an admission that the references are relevant or material to the claim. They are only cited as constituting related art of which the applicant is aware.

I hereby certify that no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in §1.56(c) more than three months prior to the filing of the information disclosure statement.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

Respectfully submitted,

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Date: 10/22/03

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